

INTRODUCTION

The National Centre for Photovoltaic Research and Education (NCPRE) at IIT Bombay announces a 3-day Workshop on "Theory and Technology of Silicon Solar Cell" on September 18 - 20, 2017.

A visit of the NCPRE laboratories would be arranged as part of the short course. The workshop would also be an excellent opportunity for networking with your peers from industry, research labs and academia.

COURSE CONTENTS

- Introduction to the physics of semiconductor devices (band diagram, optical absorption, generation - recombination, transport, pn-junction diode characteristics)
- Theory of silicon solar cells (characteristics of silicon solar cell, design of silicon solar cells - optical design, junctions, passivation, impact of these parameters on solar cell characteristics, IV measurements, quantum efficiency measurements)
- Fabrication of low-cost industrial full area Al-BSF and local area Al BSF (PERC) silicon (both mono and multi) wafer solar cells
- Simulation of solar cell using PC1D: a practical session
- Loss analysis of Al-BSF solar cells: a practical session
- Luminescence imaging of Si wafer & solar cells for process, device development and diagnostics
- Advanced silicon solar cells

WHO MAY BENEFIT

Industry personal interested in the silicon solar cell technology, including practicing engineers - personal from research labs - faculty members - Master and Ph. D. level students.

VENUE FOR CLASSES

Course will be held at VMCC , Room No: 22 ,IIT Bombay-

LECTURE NOTES

To fully realize the objectives of the course, the lecture notes/slides will be made available to the participants at the time of registration at IIT Bombay.

FACULTY

The teaching faculty constitutes experts from the Department of Electrical Engineering of IIT Bombay and invited Lecturers from Industries.

ACCOMMODATION

Accommodation on twin sharing basis is available in the Institute Guest house/Hostel for a limited number of participants on payment basis and with an advance request.

IMPORTANT DATES

Last date for receipt of registration form: 10 September, 2017.

Course dates: 18 - 20 September , 2017.

Note:

- Incomplete forms will not be accepted
- For additional copies of the registration form, please use a photocopy or type in the format given.

REGISTRATION

The course fee per participants will be as follows:

Participants	Fees
Industry personnel	18000/-*
Personnel from Academic Institutions & Government Organizations	11500/-*
Students	5750/-*

* Fees inclusive of 18% service taxes

The fee includes course material, lunch and refreshments. Limited accommodation may be available for academic participants, but is not included in the above fee.

The demand draft should be drawn in favour of **“The Registrar, IIT Bombay- CEP Account”** payable at Mumbai. For on-line payments, please [click here for Bank Details](#).

No income tax is to be deducted at source from the course fee, as IIT Bombay is exempt from the same. The course fee includes course material, lunch and coffee/tea.

A Certificate of participation will be awarded to all the participants of the course.

Completed registration forms should be sent to the course coordinator at the following address:

Ms. Smita Bhattacharjee
NCPRE, IIT Bombay
Room No: 312, 3rd floor
Transit building
Near Powerhouse, hillside area
Powai, Mumbai-400076
+91-022-25764480,
smita98@iitb.ac.in

For information on other Solar Photovoltaic (PV) courses, please visit <http://www.ncpre.iitb.ac.in/events.php>



CEP Short Term Course on

“Theory & Technology of Silicon Solar Cell”

18 - 20 September, 2017

Coordinator

Prof. Anil Kottantharayil

Department of Electrical Engineering

Office of
Continuing Education & Quality Improvement Programmes

Indian Institute of Technology Bombay Powai, Mumbai – 400 076

REGISTRATION FORM

Three-day CEP Course on

“Theroy & Technology of Silicon Solar Cell”

18 – 20 September , 2017.

NAME (BLOCK LETTERS) : _____

Gender: M / F

DESIGNATION : _____

ORGANIZATION: _____

MAILING ADDRESS : _____

TELEPHONE : _____ (O) _____ (R)

FAX: _____ MOBILE: _____

EMAIL : _____

QUALIFICATIONS : _____ EXPERIENCE : _____ Yrs.

IIT Guest House accommodation required?* YES / NO

PAYMENT: D.D. No.: _____ Dt. _____ Rs. _____

[Demand draft should be drawn in favour of "Registrar, IIT Bombay (CEP A/c)".]

Date:

Signature of Applicant

***Guest House bill to be paid directly by participant.**

(PHOTOCOPY ADDITIONAL COPIES OF THIS FORM, IF NEEDED)

On-line Payment Transaction Details

Kindly arrange to provide the following transaction details, if the course fee is paid on-line:

1. Name of the Course Participant:

2. Transaction No.:

3. Date of Transaction:

4. Amount:

5. Bank & Branch Name from where transfer is done: